## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No	7,057,263 B2
Patent Issue Date	
Application Serial No	10/743,419
Filing Date	
Assignee	Micron Technology, Inc.
Inventorship	
Attorney's Docket No	MI22-2463
Title: Semiconductor Wafer Assemblies Comprising Photoresist Over Silicon	
Nitride Materials	-

## REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR APPLICANT MISTAKES and PTO MISTAKES (37 C.F.R. §§ 1.322(a) and 1.323)

To:

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

ATTN:

Decision and Certificate of Correction
Branch of the Patent Issue Division

From:

Jennifer J. Taylor, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)

Wells St. John P.S.

601 W. First Avenue, Suite 1300

Spokane, WA 99201-3828

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 7,057,263 B2, granted June 6, 2006, in accordance with the Certificate of Correction form attached hereto.

It is noted that errors appear in this patent of a typographical nature of character, as more fully described below. The errors occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require re-examination.

The other error listed on the Certificate of Correction form was apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.

Attached hereto is Form PTO-1050 which is suitable for printing.

The exact page and line number where the errors occur in the application file are:

Page 1, Line 23

Page 7, Line 8

Page 13, Line 13

Page 16, Line 16

Claim 45, Line 4

Deposit Account 23-0925 is to be charged \$100.00 as required by 37 CFR 1.20(a).

Respectfully submitted,

Dated: Murch 9, 2007

By: Gennifer J. Taylor, Ph.D.

Reg. No. 48,711

## UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO.

: 7,057,263 B2

DATED

: June 6, 2006

INVENTOR(S)

: Moore et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, Line 9 - After "Nov. 26, 2001" insert --, now--.

Column 1, Line 40 - Replace "and is" with -- and it--.

Column 3, Line 51 - Replace "In additional" with -- In an additional--.

Column 6, Line 15 - Replace "portion 38 have" with --portion 38 and have--.

Column 7, Line 33 - Replace "to an silicon" with --to a silicon--.

Column 10, Line 33 - Replace "having an" with --having a--.

Page 1 of 1

Mailing Address of Sender:

Wells St. John P.S. 601 West First Avenue, Suite 1300 Spokane, WA 99201-3828 Patent No. <u>7,057,263 B2</u>